APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date::	03/08/04
Application Type::	Regular
Subject Matter::	Utility
Title::	Segmented Dataline Scheme in a Memory with Enhanced Full Fault Coverage Memory Cell Testability
Attorney Docket Number::	X-1292 US
Request for Early Pub?::	No
Request for Non-Pub?::	Yes
Total Drawing Sheets::	15
Small Entity?::	No
Petition included?::	No

APPLICANT INFORMATION

Applicant Authority Type::	Inventor
Primary Citizenship Ctry::	India
Status::	Full Capacity
Given Name::	Vasisht
Middle Name::	Mantra
Family Name::	Vadi
Street::	951 A, S 12th Street, #201
City::	San Jose
State or Province::	CA
Postal or Zip Code::	95112

Applicant Authority Type::	Inventor
Primary Citizenship Ctry::	US
Status::	Full Capacity
Given Name::	David
Middle Name::	P.
Family Name::	Schultz
Street::	1525 Cowper Court

City::   San Jose
State or Province::   CA
Postal or Zip Code::   95120

Applicant Authority Type::   Inventor
Primary Citizenship Ctry::   US
Status::   Full Capacity
Given Name::   Steven
Middle Name::   P.
Family Name::   Young
Street::   1180 Pintail Circle
City::   Boulder
State or Province::   Colorado
Postal or Zip Code::   80303

Applicant Authority Type::   Inventor
Primary Citizenship Ctry::   US
Status::   Full Capacity
Given Name::   Jennifer
Family Name::   Wong
Street::   40565 Encanto Way
City::   Fremont
State or Province::   CA
Postal or Zip Code::   94539

CORRESPONDENCE INFORMATION

Correspondence Customer Number::   24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	

ASSIGNEE INFORMATION

Assignee Name::	Xilinx, Inc.
Street::	2100 Logic Drive
City::	San Jose
State or Province::	California
Postal or Zip Code::	95124